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Bib Data Sheet

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** CONTINUING DATA *****

This application is a CIP of 10/192,488 07/09/2002
 and is a CIP of 10/062,106 01/31/2002 PAT 6,749,123
 and is a CIP of 10/092,681 03/07/2002 PAT 6,764,014
 and is a CIP of 10/288,945 11/06/2002
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 and claims benefit of 60/427,977 11/20/2002

** FOREIGN APPLICATIONS *****

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 09/25/2003

Foreign Priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	FL	33	42	8
Verified and Acknowledged	Examiner's Signature	Initials			

ADDRESS

20322

TITLE

METHOD FOR PREVENTING ELECTROSTATIC DAMAGE TO A SEMICONDUCTOR CHIP PACKAGE
DURING STORAGE

All Fees

**FILING FEE
RECEIVED
1696**

FEES: Authority has been given in Paper
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